[Title]				[Instructor]		
A	dvanced Op	otical and Acoustic Waves Engineering	Shoji K	akio / Satosl	ni Honma	
[Code]	[Credits]	[Program]	[Semester]	[Hours]	[Language of instruction]	
GTE501	2	Electrical and Electronic Engineering	1st Semester	Wed. ⁄ I	Japanese	
[Outline and purpose] Light and radio wave are used to carry a signal in modern optical and wireless communications, respectively. This class provide explanation of basic property of electromagnetic filed and its applications to communication/measurement techniques using wave characteristics. This class also introduce the advanced topics of optical devices such as light generators, detectors, and analyzers, and specific systems such as optical communication and information processing. [Objectives] 1. to understand the nature of the phenomenon of wave movement and to express its characteristics mathematically 2. to derive wave equations from Maxwell equations and further apply boundary conditions 3. to analysis light wave propagating in free space and wave-guide. [Requirements] Requirements for admission to the course are basic mathematical knowledge such as calculus, linear algebra, and basic analysis and electromagnetics engineering. [Evaluation] 1. Final Exam : 100%						
[Textbooks]						
[References]					
	-					
[Schedule]						
 Derivat Fourier Fourier Fourier Fourier Optical Optical Polariz 2-wave Wave p Transm Electro Mode c 	tion of wav optics and beam pro- beam pro- lens and I lens and I ation and I interferen ropagation hission disp magnetic f oupling (co oupling (co	and Basics of vector operation e equation for plane wave, and its phase velocid beam propagation analysis in free space pagation method and Fresnel-Kirchhoff integra pagation method and Fresnel-Kirchhoff integra Fourier transform (1) Fourier transform (2) Fresnel coefficients ce / multiple interference in wave-guide persion equation and dispersion curve by wave field distribution of guided mode ordirectional coupling) intra-directional coupling)	ral theorem(1) ral theorem(2)			

		[Title]		[Instructor	•]	
	Advance	ed Electronic Device Engineering	Norio Onoj	Norio Onojima / Koji Yano/ Masa Yamamoto		
[Code]	[Credits]	[Program]	[Semester]	[Hours]	[Language of instruction]	
GTE503	2	Electrical and Electronic Engineering	2nd Semester	Thu./II	Japanese	
This cours		the knowledge to understand the principle ented society in recent years.	of semiconductor	devices as	key devices for	
inorganic 2. To under	rstand the s counterpar rstand the fu	fundamental physics of organic semicondu ts andamental physics of semiconductor power			compared with	
[Requirem Basic know		ectromagnetism and Semiconductor Enginee	pring			
[Evaluation Test / Repo [Textbooks] Original te	rt 100%	sed.				
[Reference Appropriat		s will be introduced during the course.				
[Schedule]						
 (2) Fundan (3) Carrier (4) Device p (5) Fabrica (6) Applica (7) Introdu (8) Trend o (9) Structu (10) Struct (11) Struct (12) Struct (13) Wide k (14) Wide k 	transport m physics of or tion process tions of orga ction of orga f power devi re and phys ure and phy ure and phy ure and phy oandgap pow	acs of organic semiconductors nechanism in organic semiconductors ganic transistors of organic transistors anic transistors anic semiconductor-based optoelectronic devi ice development ics of pin diode rsics of power MOSFET sics of IGBT rsics of superjunction power device ver devices : SiC power devices ver devices : GaN power devices	ices			

		[Title]		[Instructor]	
	Ad	vanced Crystal Engineering	Tsutomu M	uranaka / Yo	oichi Nabetani	
[Code]	[Credits]	[Program]	[Semester]	[Language of instruction]		
GTE504	2	Electrical and Electronic Engineering	2nd Semester	Mon.∕II	Japanese	
[Outline and purpose] [Outline and purpose] Crystal engineering, the design and formation of solid-state structures, is a key technology for semiconductor devices used in various optical and electronic applications. This course provides the knowledge of crystal growth and processes for semiconductor device fabrication. You will learn up-to-date information about crystal growth and processes for semiconductor device fabrication from R&D phase to industrial product phase. [Objectives] By the end of the course, you will be able to understand and describe the bases of crystal growth and processes for semiconductor device fabrication. Also you will be able to understand and describe many kinds of semiconductor technology in today's world.						
[Requireme It is desira devices.		ou have learned the bases of calculus, physics,	inorganic che	emistry and	semiconductor	
[Evaluation Test / Repo [Textbooks]	rt 100%					
	s] written in a	Japanese are shown in the Japanese syllabus. ng-K. Lee, Semiconductor Devices: Physics and	Technology, T	hird Edition	, Wiley (ISBN:	
978-047053 [Schedule] 01. Introdu 02. Fundan 03. Materia 04. Method 05. Equipm 06. Charact 07. Charact 08. Charact 09. Single of 10. Single of 11. Physics 12. Process 13. Process	ction to crymentals of epitals s of epitals of epitals s of epitals terization of terization of terization of terization of terization of terization of terization of terization of teristal grow and techno for semicor for semicor for semicor	stal growth and epitaxy pitaxial growth xial growth	ion			

		[Title]		[Instructor	r]
	Advanc	ed Signal and Systems Engineering	Makoto C)hki / Masar	nori Hanawa
[Code]	[Credits]	[Program]	[Semester]	[Hours]	[Language o instruction]
$\operatorname{GTE505}$	2	Electrical and Electronic Engineering Embedded and Integrated System Development	1st Semester	Tue. / II	Japanese / English
Digital Si systems. fundamen filter desig DSP techr modulus a [Objective 4. to und 5. to ana 6. to desi 7. to exp] [Requirem	This class tals on sigr gn techniqu iques used lgorithm an s] erstand the lysis signal gn basic fil lain the pun ents]	ssing (DSP) techniques are widely applied in m covers wide range of DSP techniques from fu- nals and systems analysis, Discrete Fourier Trans- ies, adaptive signal processing, multi-dimensional in digital coherent fiber-optic communication syst ad digital back propagation techniques and so on.	ndamentals form or Fast l signal proce cems such as place transfor pcessing techr	to applicati Fourier Tra ssing, and s phase estim rm and the z	ons, includin nsform, digita state of the ar ation, constan
 final expression Textbooks J. H. M Sayed M. Na 	xamination 5] AcClellan, I Ali H., Ada	tion or report: 50% or report: 50% R. W. Schafer, and M. A. Yoder, DSP First Second E aptive Filters, Wiley, 2008. K. Kikuchi, T. Miyazaki, High Spectral Density			
[Reference	es]	signments would be given arbitrarily.			
 Fourie Funda Funda Digita Statist Adapti Arraye Multi- (The above Shann Lasers Extern Optica Multi- Multi- Multi- Linear 	mentals on l filter designing ive signal pre- dimensional e eight class on's channed and optical r l amplifiers level modul e and non-li cments com	a and frequency domain analysis digital filters gn processing and optimal filters rocessing ocessing l filters and nonlinear filters ses would be given by Prof./Dr. Makoto Ohki) el capacity and brief overview of fiber-optic commu	nodulators, an		re modulators

		[Title]		[Instructor	:]
	Advano	ced Electronic Circuits Engineering	Takahi	ide Sato/Nao	to Sekiya
[Code]	[Credits]	[Program]	[Semester]	[Hours]	[Language of instruction]
GTE506	2	Electrical and Electronic Engineering Embedded and Integrated System Development	1st Semester"	Mon.∕II	Japanese / English
Very Larg sophistica and pract from fund amplifiers technique [Objective 1. to expla 2. to desig 3. to expla 4. to analy [Requirem Basic know [Evaluatio final exam	tion, minia ical mixed amentals t , Filters, I <u>using discr</u> s] in characte n a basic ar in and desi <u>vse a DC-DC</u> ments] wledge of el on] ination or n s] aterials abo	tegrated circuits (VLSI) are widely used in mode turization and high reliability. The goal of this cl signal integrated circuits. This class covers wide o applications, including fundamentals on MOS T Data convertors, Phase lock loop and so on. Fu ete devices including switching DC-DC convertor d ristics and usage of MOSFETs. halog integrated circuits used in analog integrated of gn an operational amplifier, a filter, ADC and PLL.	lass is to lear range of VLS Transistor and rthermore, tl esign is also l circuits.	rn how to de SI circuit de d its analysi he trend of	sign the latest sign technique s, Operational
 MOSFE Single s Operati Operati Filters Filters Digital Analog Oscilla Simula Power 	heorem of el Tage amplif onal amplif 1 (Performa 2 (Active fil- to analog co to digital co ators and Pl ator and La supply circ	iers 1 (General considerations, Performance param iers 2 (Two stage operational amplifiers, Slew rate, nce parameters, Design of transfer function) ter, Switched capacitor filter) onvertors nvertors LL			

[Title]				[Instruct	or]		
Advanced Measurement Engineering			Chen Lee	e Chuin / Sa	toshi Ninomiya		
[Code]	[Credits]	[Program]	[Semester]	[Hours]	[Language of instruction]		
$\operatorname{GTE507}$	2	Electrical and Electronic Engineering	1st Semester	Thu./ Mon./ III	Japanese/English		
[Outline and purpose]							
Sensing an sensory sys basic science and the fu electron sp	Sensing and measurement are indispensable to the advancement of science and technology as if the human sensory system to our daily life. The measurement technologies have multiple impacts on the development of basic sciences as well as on the commercial R&D. In this course, student will learn about the latest development and the fundamental principle behind the widely used scientific instruments such as electron microscope, electron spectroscope and mass spectrometer. Recent research topics on the in-situ biological analysis and imaging mass spectrometry will also be reviewed.						
[Objectives]]						
Describe th	e related v	acuum technologies used in the advanced mea	asurement ins	trument.			
-		behind the measurement and sensing techno	0				
Describe th	e applicatio	on of measurement technologies in the pursui	t of basic scier	nce and com	nercial R&D.		
[Evaluation	_						
Test, quiz a Attendance		70%) ng attitude (30%)					
[Textbooks]							
Materials a	nd lecture	notes will be distributed.					
[References	.1						
Nil	5]						
INII							
[Schedule]							
1. Genera	l introducti	on					
		vacuum technology					
	-	ind measurement					
	 High voltage and gaseous breakdown Electron beam technology 						
		hnology .ical technology					
-	-						
-	1 00						
		y and ionization methods					
11. Isotope	-						
		ace analysis tors					
10. 50116016	3. Sensors and Detectors						

- 14. In-situ biological analysis and imaging mass spectrometry
- 15. Review and conclusion

		[Title]		[Instructor	r]	
	Advanc	ed Electrical Power Engineering		Kazuyuki U	Ino	
[Code]	[Credits]	[Program]	[Semester]	[Semester] [Hours]		
GTE508	2	Electrical and Electronic Engineering	1st Semester	Fri./I	Japanese	
paradigm s renewable present nee and energy [Objectives Students co 1. be able power u 2. be able 3. be able 4. be able	East Japan shift of an energy resc eds and futu conversion.] ompleting th to explain use, and ener to explain a to explain a to explain a ents] nts for admi	Earthquake in 2011 gave big damage to nucle energy supply system. This course provides purces with a scientific examination of the e ure energy demands and focuses on electric powe me course will about history of electric energy, electric powe ergy conversion. about energy resource, fossil energy, and nuclea about thermal energy and heat pump technolog about chemical energy, fuel cell, and hydrogen e	an introduction nergy field. The er generation, e r generation, e r energy. y. mergy system.	on to energ le course ex electric powe	y systems and plores society' er transmission r transmission	
Final exam	d homework ination and	x assignments 35% presentation 65% 基礎エネルギー工学, Suurikougakusha-sha, ISI	3N4901683047			
[References 1. S. T. Pa		ang, Introduction to high power pulse technolo	gy, World Scien	tific, ISBN9	810217145	
 Curren History Electric Electric Power of Energy Nuclear Nuclear Nuclear Therma Heat po 	t status and of electric e city business power gene electronics t resource r energy r fusion and al dynamics al dynamics ump technol	s in Japan eration, electric power transmission, and energ echnology and quizzes				

- 14. Hydrogen energy and fuel cell
- 15. Final examination and presentation

[Title]			[Instructor]				
Adv	vanced Pow	er Semiconductor Modules Engineering	Y. Ike	eda, N. Eguc	hi et al.		
[Code]	[Credits]	[Program]	[Semester]	[Hours]	[Language of instruction]		
GTE509	2	Electrical and Electronic Engineering	1st Semester	Tue. / III	Japanese		
[Outline an	d purpose]		I				
trains and on physics	Semiconductor power device is a key technology supporting today's our life from home-electronics to cars, trains and industries. Researchers and engineers in the forefront of major power-device industry give lectures on physics and technology of power semiconductor devices emphasizing packaging technology. You will learn up-to-date state of power devices from R&D phase to industrial product phase.						
[Objectives]							
By the en thermal an	d of the co d structura	burse, you will be able to understand and dese l design and insulation technique of power mo understand and describe many kinds of power-e	dules, and re	liability of p	ower modules.		
[Requireme	nts]						
-	able that yo	ou have bases of Semiconductor devices, Electric	cal circuit, Ele	ectronic circu	uit and Electro		
[Evaluation	1						
-	-	2. Midterm Exam : 15%, 3. Small Quizzes /Re	ports: 10%				
		r in Class : 20% 5. Presentation : 20%	ports · 10/0				
[Textbooks]							
[References]						
[Schedule]							
[Schedule]1. Basic physics of power semiconductor modules2. Thermal and structural design of power semiconductor modules3. Insulation technique of power semiconductor modules4. Tour of Matsumoto Factory and Omachi Factory of Fuji Electric Co.5. Materials and reliability of power semiconductor modules6. Power Electronics -how to use power devices-7. The latest trend of power transforming equipment (Electric car, inverter, UPS)8. Tour of Tokyo Factory of Fuji Electric Co.9. Application of power transforming equipment (Shinkansen train etc.)10. Future of power semiconductors and power electronics11. Tour of Yamanashi Factory of Fuji Electric Co.							