		[Title]		[Instructor	·]		
Advanced Electronic Device Engineering			Norio Onoj	Norio Onojima / Koji Yano/ Masayuki Yamamoto			
[Code]	[Credits]	[Program]	[Semester]	[Hours]	[Language of instruction]		
GTE503	2	Electrical and Electronic Engineering	2nd Semester	Thu./II	Japanese		
highly info	e provides t rmation-orie	he knowledge to understand the principle of nted society in recent years.	semiconductor	devices as	key devices for		
inorganic	rstand the f counterpart	fundamental physics of organic semiconductors indamental physics of semiconductor power de			compared with		
[Requireme Basic know		ctromagnetism and Semiconductor Engineerin	ıg				
[Evaluation Test / Repor	rt 100%						
[Textbooks] Original te	xt will be us	ed.					
[References Appropriat	-	will be introduced during the course.					
 (2) Fundam (3) Carrier (4) Device p (5) Fabricat (6) Applicat (7) Introduct (8) Trend of (9) Structur (10) Structur (11) Structur (12) Structur (13) Wide b (14) Wide b 	transport m bhysics of org tion process tions of orga tion of orga f power device re and physic are and physic are and physic are and physic are and physic are and physic are and physic	electronics cs of organic semiconductors echanism in organic semiconductors ganic transistors of organic transistors nic transistors nic semiconductor-based optoelectronic devices ce development cs of pin diode sics of power MOSFET sics of IGBT sics of superjunction power device er devices : SiC power devices er devices : GaN power devices	5				

[Title]			[Instructor]			
Advanced Crystal Engineering			Tsutomu Muranaka / Yoichi Nabetani			
[Code]	[Credits]	[Program]	[Semester] [Hours] [Language instruction			
GTE504	2	Electrical and Electronic Engineering	2nd Semester	Mon.∕II	Japanese	
[Outline and purpose] Crystal engineering, the design and formation of solid-state structures, is a key technology for semiconductor devices used in various optical and electronic applications. This course provides the knowledge of crystal growth and processes for semiconductor device fabrication. You will learn up-to-date information about crystal growth and processes for semiconductor device fabrication from R&D phase to industrial product phase. [Objectives] By the end of the course, you will be able to understand and describe the bases of crystal growth and processes						
		vice fabrication. Also you will be able to un ogy in today's world.	derstand and	l describe r	nany kinds of	
[Requireme It is desira devices.		ou have learned the bases of calculus, physics,	inorganic che	emistry and	semiconductor	
[Evaluation						
Test / Repor	rt 100%					
[Textbooks]						
Original tex	t will be us	sed.				
[References						
	Sze and Mi	Japanese are shown in the Japanese syllabus. ng-K. Lee, Semiconductor Devices: Physics and	Technology, T	hird Edition	, Wiley (ISBN:	
[Schedule]						
 01. Introduction to crystal growth and epitaxy 02. Fundamentals of epitaxial growth 03. Materials for epitaxial growth 04. Methods of epitaxial growth 05. Equipment for epitaxial growth 						
 06. Characterization of epitaxial growth: Structural properties 07. Characterization of epitaxial growth: Optical properties 08. Characterization of epitaxial growth: Electrical properties 09. Single crystal growth of silicon: Czochralski method 10. Single crystal growth of silicon: Float zone method 						
 Process Process Process Process 	 11. Physics and technology of silicon MOSFET 12. Process for semiconductor device fabrication: Oxidation and deposition 13. Process for semiconductor device fabrication: Metallization 14. Process for semiconductor device fabrication: Lithography and etching 15. Final examination 					

[Title]			[Instructor]			
Advanced Signal and Systems Engineering Makoto Ohki / Masanori Ha				ori Hanawa		
[Code]	[Credits]	[Program]	[Semester]	[Hours]	[Language of instruction]	
GTE505	2	Electrical and Electronic Engineering Embedded and Integrated System Development	1st Semester	Tue./II	Japanese / English	
[Outline and purpose] Digital Signal Processing (DSP) techniques are widely applied in modern information and communication systems. This class covers wide range of DSP techniques from fundamentals to applications, including fundamentals on signals and systems analysis, Discrete Fourier Transform or Fast Fourier Transform, digital filter design techniques, adaptive signal processing, multi-dimensional signal processing, and state of the art DSP techniques used in digital coherent fiber-optic communication systems such as phase estimation, constant modulus algorithm and digital back propagation techniques and so on. [Objectives] 1. to understand the purpose of signal processing						
3. to desi	gn basic filt	s and systems using the Fourier transform, the Lap ters pose and the characteristics of advanced signal pro			-transform	
[Requirem Fundamer MATLAB		edge of mathematics such as calculus, linear al	gebra and co	omplex num	ber. Usage of	
[Evaluatio	n]					
		tion or report: 50% or report: 50%				
[Textbooks]					
 J. H. McClellan, R. W. Schafer, and M. A. Yoder, DSP First Second Edition, Prentice Hall, 2015. Sayed, Ali H., Adaptive Filters, Wiley, 2008. M. Nakazawa, K. Kikuchi, T. Miyazaki, High Spectral Density Optical Communication Technologies, Springer, 2010. 						
[Reference						
Additional	reading as	signments would be given arbitrarily.				
[Schedule]						
 Signals and systems Fourier transform and frequency domain analysis Fundamentals on digital filters Digital filter design Statistical signal processing and optimal filters 						
 Adaptive signal processing Arrayed signal processing Multi-dimensional filters and nonlinear filters (The above eight classes would be given by Prof./Dr. Makoto Ohki) 						
 Shannon's channel capacity and brief overview of fiber-optic communication systems Lasers and optical fibers External optical modulators including intensity modulators, phase modulators, and quadrature modulators 						
13. Multi-	 12. Optical amplifiers and wavelength division multiplexing techniques 13. Multi-level modulation formats and phase diversity receivers 14. Linear and non-linear distortions under transmission in optical fibers 					
15. Impain	15. Impairments compensation techniques (The above seven classes would be given by Prof./Dr. Masanori Hanawa)					

	[Title]		[Instructor	r]			
Advanced Electronic Circuits Engineering			Takahide Sato/Naoto Sekiya				
[Code] [Credits]	[Program]	[Semester]	[Hours]	[Language of instruction]			
GTE506 2	Electrical and Electronic Engineering Embedded and Integrated System Development	1st Semester''	Mon.∕II	Japanese / English			
[Outline and purpose] Very Large Scaled Integrated circuits (VLSI) are widely used in modern electronics systems to achieve their sophistication, miniaturization and high reliability. The goal of this class is to learn how to design the latest and practical mixed signal integrated circuits. This class covers wide range of VLSI circuit design technique from fundamentals to applications, including fundamentals on MOS Transistor and its analysis, Operational amplifiers, Filters, Data convertors, Phase lock loop and so on. Furthermore, the trend of circuit design technique using discrete devices including switching DC-DC convertor design is also lectured. [Objectives] 1. to explain characteristics and usage of MOSFETs. 2. to design a basic analog integrated circuits used in analog integrated circuits. 3. to explain and design an operational amplifier, a filter, ADC and PLL. 4. to analyse a DC-DC convertor. [Requirements] Basic knowledge of electric circuit, electronic circuit and circuit theory							
[Textbooks]							
Printed materials ab	out lecture topics will be distributed during the lect	oure.					
[References]							
[Schedule]							
5. Operational ampli 6. Filters 1 (Perform 7. Filters 2 (Active fi 8. Digital to analog c 9. Analog to digital c 10. Oscillators and F 11. Simulator and La 12. Power supply circ	fier fiers 1 (General considerations, Performance param fiers 2 (Two stage operational amplifiers, Slew rate, ance parameters, Design of transfer function) lter, Switched capacitor filter) onvertors onvertors LL						

[Title]				[Instructor]			
	ed Measurement Engineering	Chen Lee	Chen Lee Chuin / Satoshi Ninomiya				
[Code]	[Credits]	[Program]			[Language of instruction]		
GTE507	2	Electrical and Electronic Engineering	1st Semester	Thu./ Mon./ III	Japanese/English		
[Outline an							
sensory sys basic science and the fu electron sp	Sensing and measurement are indispensable to the advancement of science and technology as if the human sensory system to our daily life. The measurement technologies have multiple impacts on the development of basic sciences as well as on the commercial R&D. In this course, student will learn about the latest development and the fundamental principle behind the widely used scientific instruments such as electron microscope, electron spectroscope and mass spectrometer. Recent research topics on the in-situ biological analysis and imaging mass spectrometry will also be reviewed.						
[Objectives]]						
Explain the	e principles	acuum technologies used in the advanced mea behind the measurement and sensing techno on of measurement technologies in the pursui	logies.		mercial R&D.		
[Evaluation	1]						
Test, quiz a Attendance		70%) ng attitude (30%)					
[Textbooks]							
Materials a	ind lecture	notes will be distributed.					
[References	s]						
Nil							
[Schedule]	limtur less.						
 Fundar Vacuum High vo Electron 	 Vacuum analysis and measurement High voltage and gaseous breakdown 						
 X-ray beam analytical technology Optical beam technology Ion beam technology Scanning probe technology 							
 10. Mass spectrometry and ionization methods 11. Isotope analysis 12. Surface and interface analysis 							
14. In-situ	 Sensors and Detectors In-situ biological analysis and imaging mass spectrometry Review and conclusion 						

[Title]			[Instructor]			
Advanced Electrical Power Engineering			Kazuyuki Uno			
[Code]	[Credits]	[Program]	[Semester]	[Hours]	[Language of instruction]	
GTE508	2	Electrical and Electronic Engineering	1st Semester	Fri./I	Japanese	
[Outline and purpose] The Great East Japan Earthquake in 2011 gave big damage to nuclear power plants and was a trigger of a big paradigm shift of an energy supply system. This course provides an introduction to energy systems and renewable energy resources with a scientific examination of the energy field. The course explores society's present needs and future energy demands and focuses on electric power generation, electric power transmission and energy conversion. [Objectives] Students completing the course will 1. be able to explain about history of electric energy, electric power generation, electric power transmission, power use, and energy conversion. 2. be able to explain about energy resource, fossil energy, and nuclear energy. 3. be able to explain about thermal energy and heat pump technology. 4. be able to explain about chemical energy, fuel cell, and hydrogen energy system. [Requirements] Requirements for admission to the course are basic knowledge of high voltage engineering, electric engineering,						
and physics]					
		x assignments 35% l presentation 65%				
[Textbooks] 1. Makoto	Katsurai,	基礎エネルギー工学, Suurikougakusha-sha, ISBI	N4901683047			
[References	1					
		ang, Introduction to high power pulse technology	y, World Scient	tific, ISBN98	310217145	
[Schedule]						
 Work, energy, and power revolution Current status and problems of primary energy, and quizzes History of electric energy Electricity business in Japan Electric power generation, electric power transmission, and energy conversion Power electronics technology and quizzes Energy resource Nuclear energy Nuclear fusion and quizzes Thermal dynamics Thermal dynamics in thermal power generation and nuclear power generation Heat pump technology and quizzes Chemical energy and battery Hydrogen energy and fuel cell Final examination and presentation 						

[Title]			[Instructor]					
Advanced Power Semiconductor Modules Engineering			Y. Ike	Y. Ikeda, N. Eguchi et al.				
[Code]	[Credits]	[Program]	[Semester]	[Hours]	[Language of instruction]			
GTE509	2	Electrical and Electronic Engineering	1st Semester	Tue./III	Japanese			
Semicondu trains and on physics	[Outline and purpose] Semiconductor power device is a key technology supporting today's our life from home-electronics to cars, trains and industries. Researchers and engineers in the forefront of major power-device industry give lectures on physics and technology of power semiconductor devices emphasizing packaging technology. You will learn up-to-date state of power devices from R&D phase to industrial product phase.							
By the en thermal an Also you wi	d of the co d structura ll be able to	purse, you will be able to understand and deso l design and insulation technique of power mo o understand and describe many kinds of power-e	dules, and re	liability of p	ower modules.			
magnetism.	able that yo	ou have bases of Semiconductor devices, Electric	cal circuit, Ele	ectronic circu	uit and Electro			
1. Final E	[Evaluation] 1. Final Exam : 35%, 2. Midterm Exam : 15%, 3. Small Quizzes /Reports : 10% 4. Attendance/Behavior in Class : 20% 5. Presentation : 20% [Textbooks]							
[References]							
[Schedule]								
 Basic physics of power semiconductor modules Thermal and structural design of power semiconductor modules Insulation technique of power semiconductor modules Tour of Matsumoto Factory and Omachi Factory of Fuji Electric Co. Materials and reliability of power semiconductor modules Power Electronics -how to use power devices- The latest trend of power transforming equipment (Electric car, inverter, UPS) Tour of Tokyo Factory of Fuji Electric Co. Application of power transforming equipment (Shinkansen train etc.) Future of power semiconductors and power electronics Tour of Yamanashi Factory of Fuji Electric Co. 								